
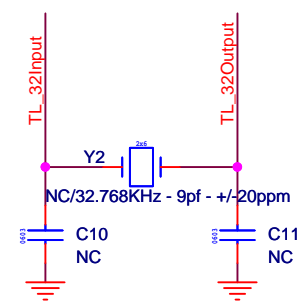
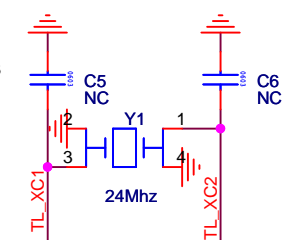
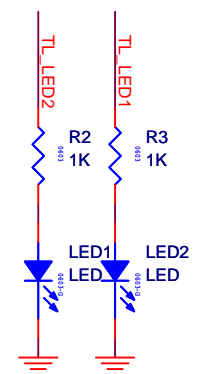
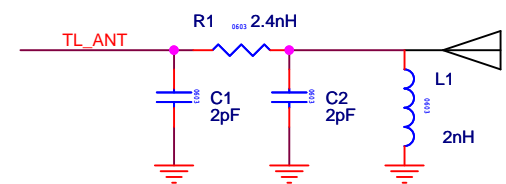
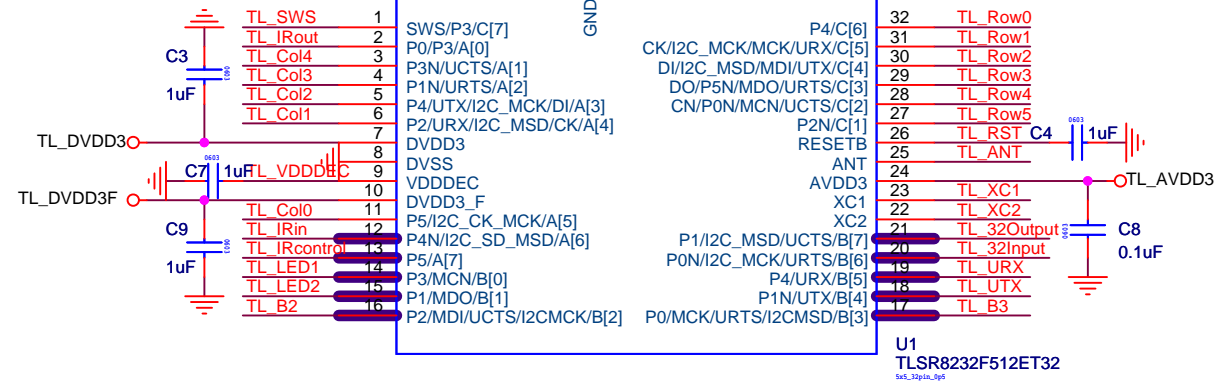


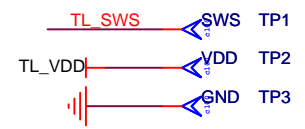
All GPIOs		wake up source
All GPIOs		10K/1M Up 100K Down
	ADC Channels	
PB7	Bat_Det Channels	

>>TL_Row0
 >>TL_Row1
 >>TL_Row2
 >>TL_Row3
 >>TL_Row4
 >>TL_Row5

>>TL_Col0
 >>TL_Col1
 >>TL_Col2
 >>TL_Col3
 >>TL_Col4



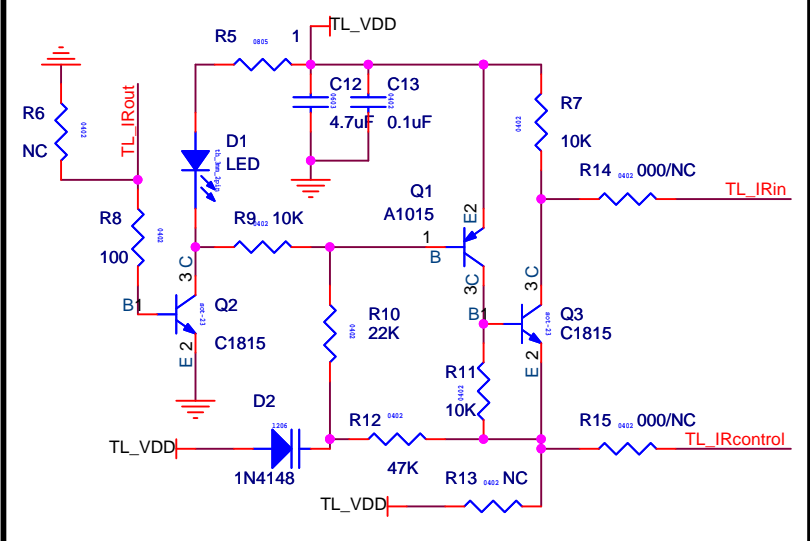
DownLoad



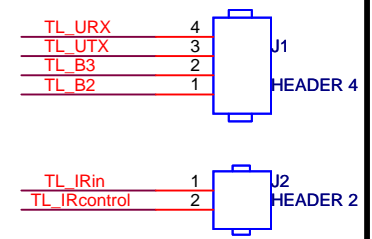
- TL_Col0 TP4 A5
- TL_Col1 TP5 A4
- TL_Col2 TP6 A3
- TL_Col3 TP7 A2
- TL_Col4 TP8 A1
- TL_Row0 TP9 C6
- TL_Row1 TP10 C5
- TL_Row2 TP11 C4
- TL_Row3 TP12 C3
- TL_Row4 TP13 C2
- TL_Row5 TP14 C1
- TL_Irout TP15 A0
- TL_LED1 TP18 B0
- TL_LED2 TP24 B1
- TL_32Output TP22 B7
- TL_32Input TP23 B6

IR TX

TL_Ircontrol和TL_Irin对应的管脚用做Debug GPIO时，R14和R15不焊



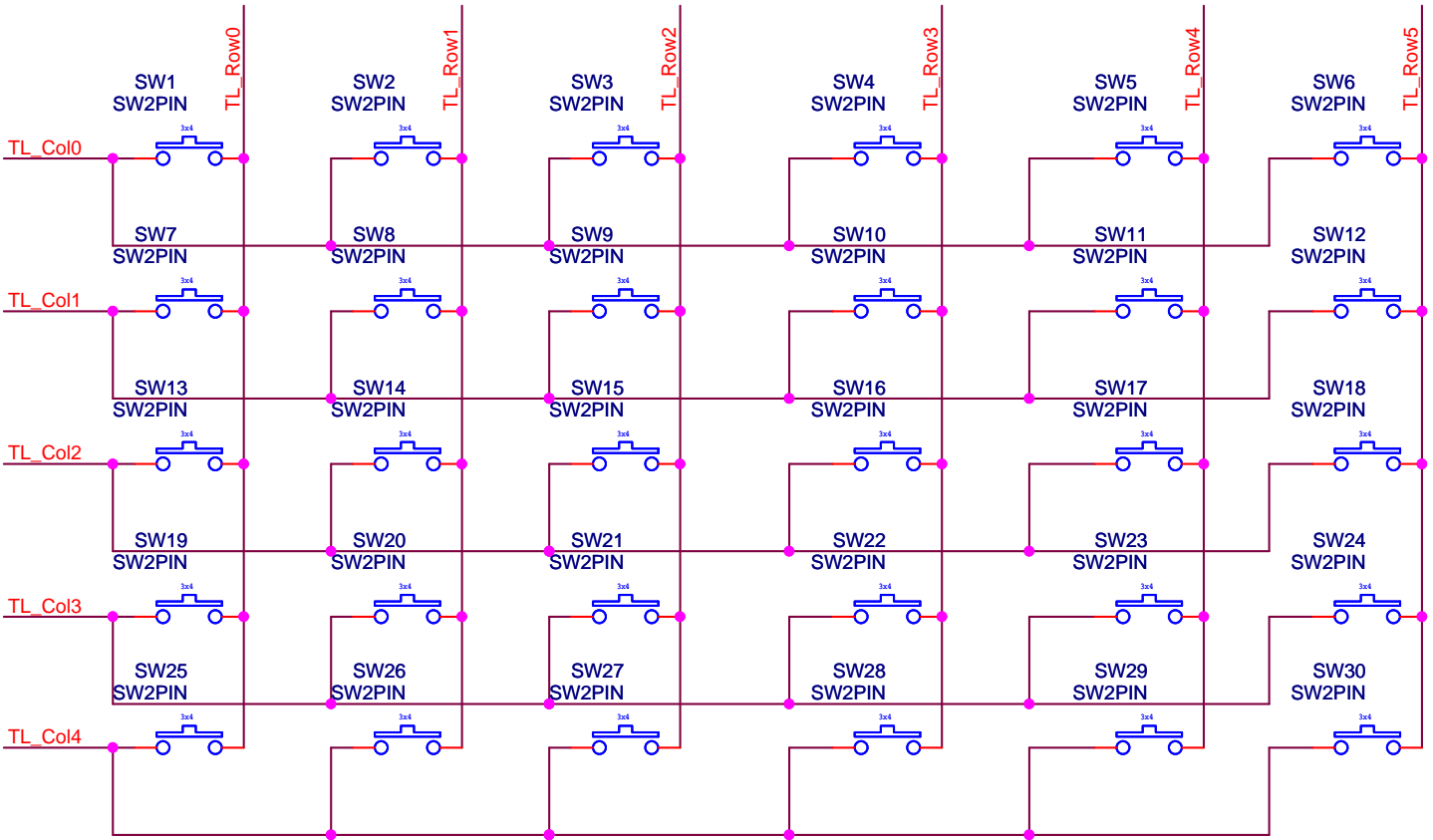
Debug IO



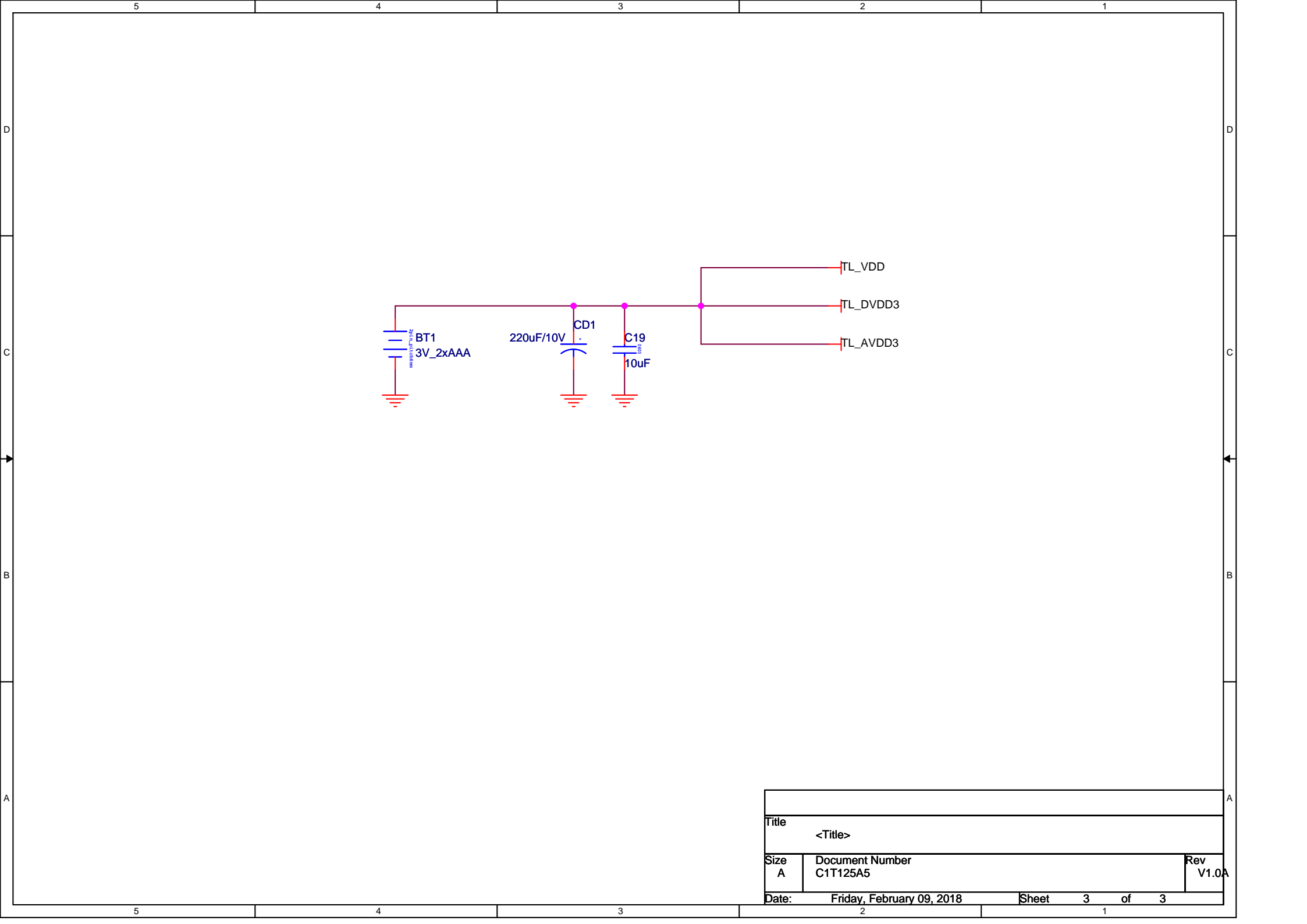
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Telink-Semi		
Size	Document Number	Rev
A4	C1T125A5	V1.0A
Date:	Friday, February 09, 2018	Sheet 1 of 3

>>TL_Row0
>>TL_Row1
>>TL_Row2
>>TL_Row3
>>TL_Row4
>>TL_Row5

>>TL_Col0
>>TL_Col1
>>TL_Col2
>>TL_Col3
>>TL_Col4



Title		
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Size A	Document Number C1T125A5	Rev V1.0A
Date:	Friday, February 09, 2018	Sheet 2 of 3



Title			
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Size	Document Number		Rev
A	C1T125A5		V1.0A
Date:	Friday, February 09, 2018	Sheet	3 of 3