

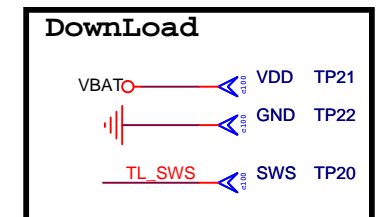
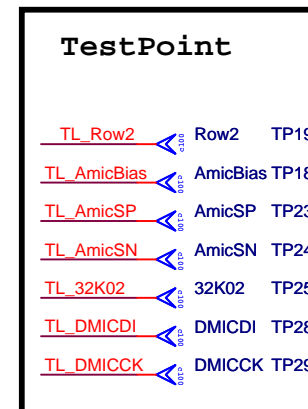
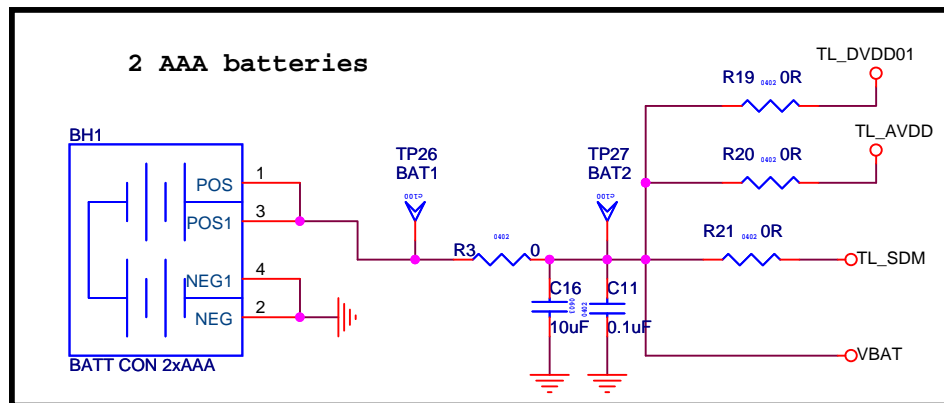
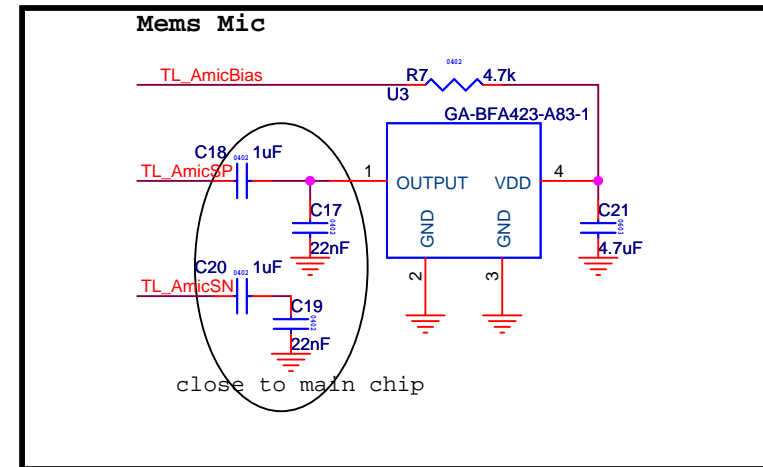
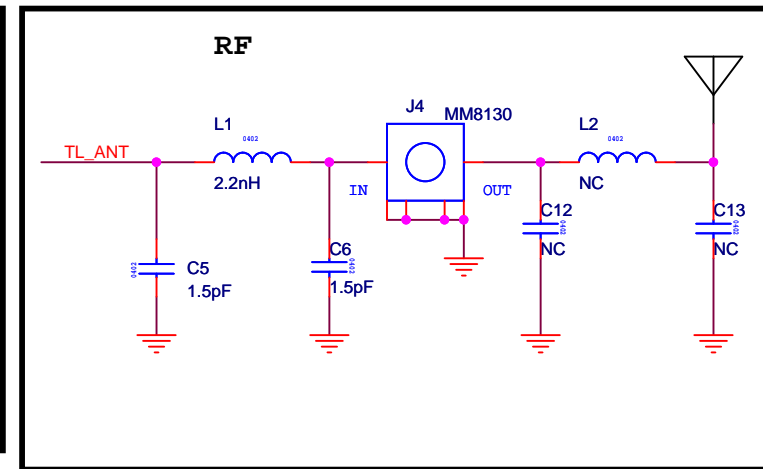
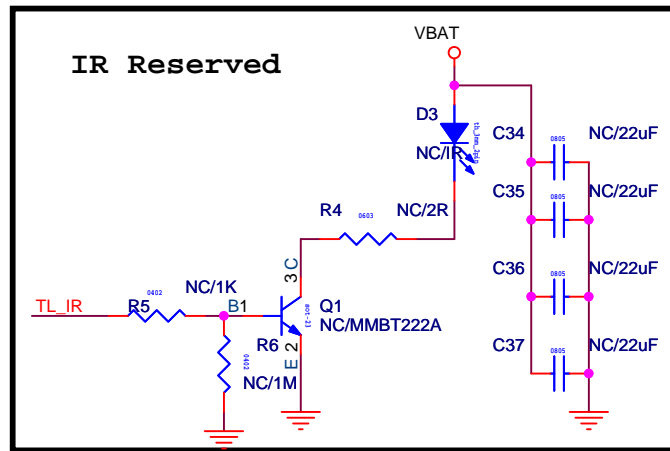
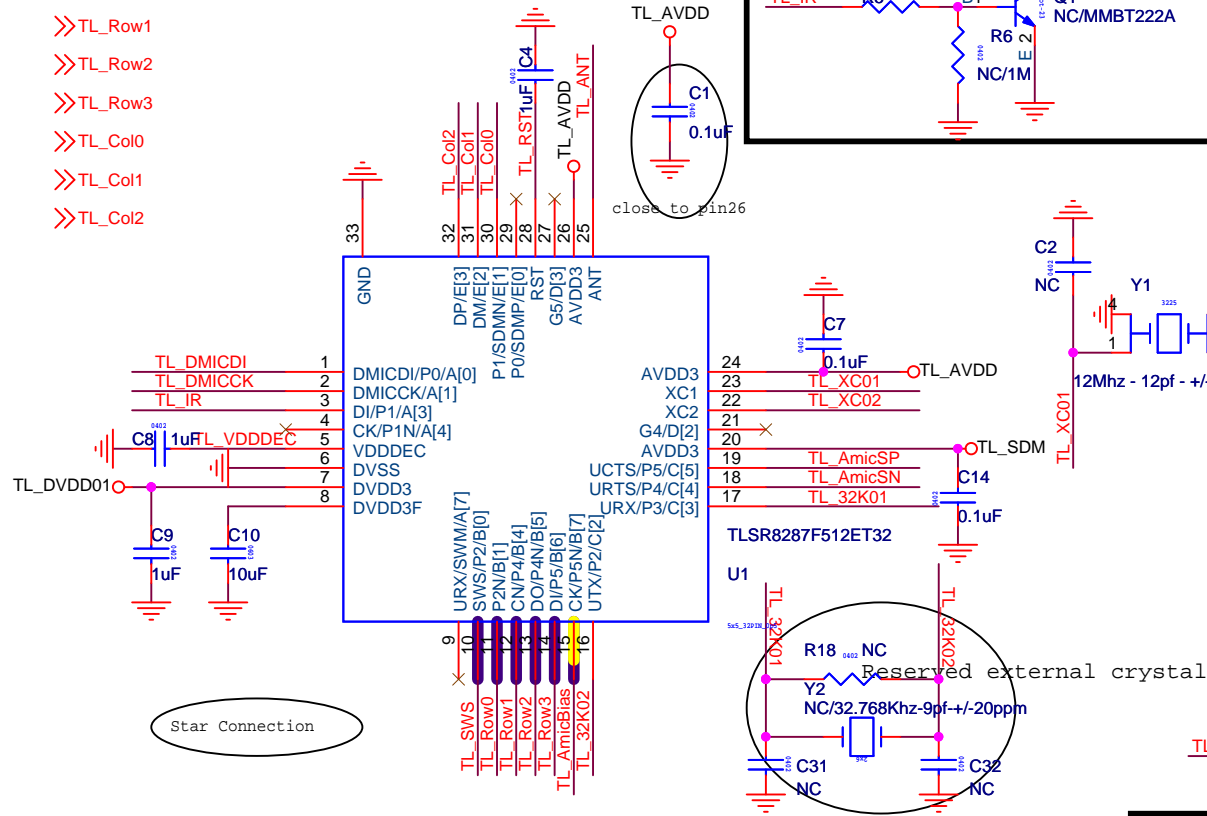


All GPIOs	wake up source
All GPIOs	10K/1M Up 100K Down
	ADC Channels
	Bat_Det Channels

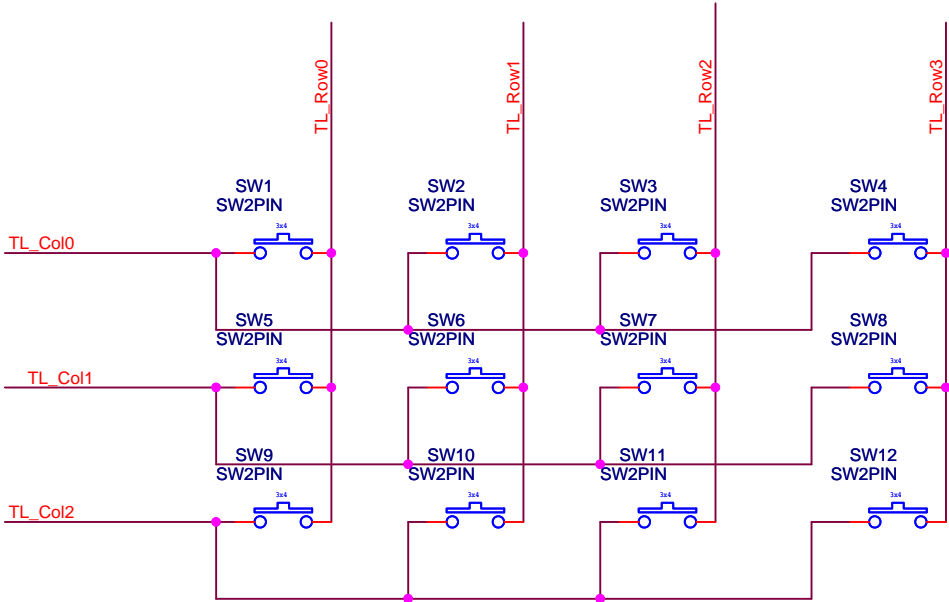
>>TL\_Row0  
>>TL\_Row1  
>>TL\_Row2  
>>TL\_Row3  
>>TL\_Col0  
>>TL\_Col1  
>>TL\_Col2



Title		
Telink-Semi		
Size	Document Number	
	C71T121A5	
Date:		Rev
Friday, July 07, 2017		V1.2
Sheet		1 of 1

>>TL\_Row0  
>>TL\_Row1  
>>TL\_Row2  
>>TL\_Row3  
>>TL\_Col0  
>>TL\_Col1  
>>TL\_Col2

Key



Title			
Telink-Semi			
Size	Document Number		Rev
A4	C71T121A5		V1.2
Date:	Friday, July 07, 2017		Sheet 1 of 1