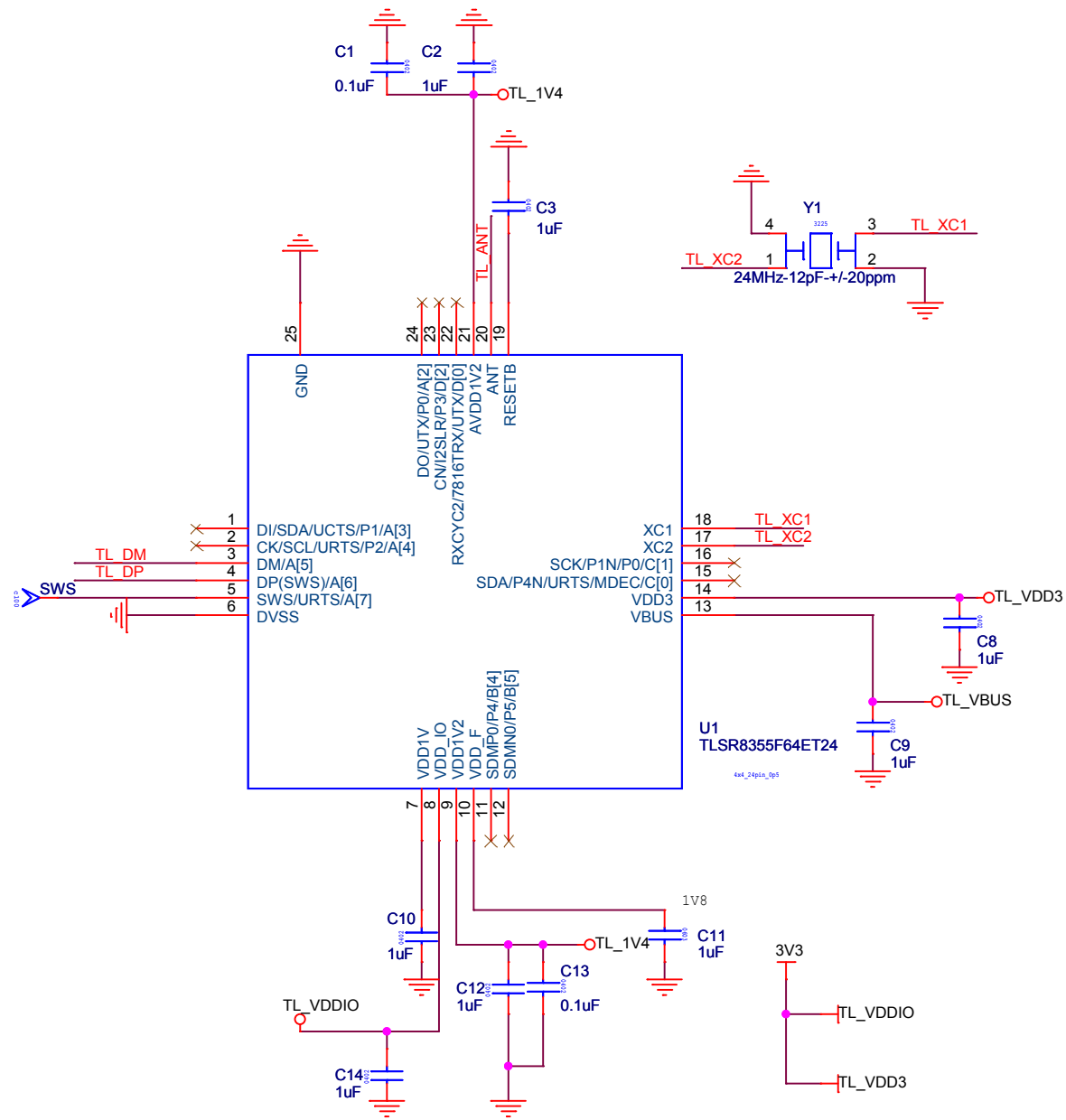
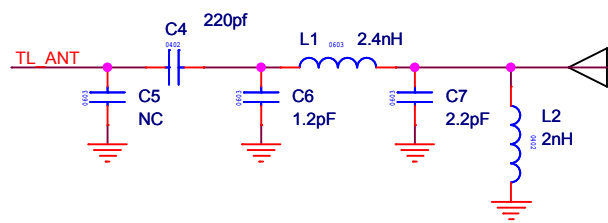


Date	Version	Update	Designer	Reviewer
2019-10-12	v1.0	Initial Version		
2020-04-02	v1.1	Add sws test point	Hongwei	Lixiang
2020-04-10	V1.1	Update value, C9=NC C10=1.2pF L4=2.4nH C11=2.2pF Component manufacturer: Murata Chip log: TLSR8355F128ET24 ZHL2010 EP7945.41	Hongwei	Lixiang

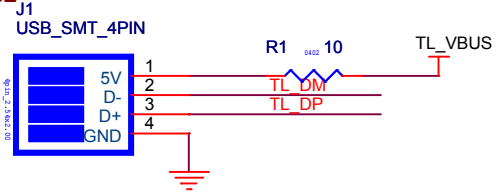
Main Chip



ANT



Power



Title									
Telink-Semi									
Size A4		Document Number C1T195A3							Rev V1.1
Date: Monday, April 13, 2020			Sheet 2		of 2				
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